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Semiconductor Laser Conference, 2002. IEEE 18th International , 2002

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Nishigaki, M.; Saka, M.; Aoki, T.; Yuhara, H.; Kawai, M.;

Intelligent Vehicles Symposium, 2000. IV 2000. Proceedings of the IEEE , 20

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3 Universal digital sequential circuit programmable wavelet array

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4 Mixed mode circuit simulator SPLIT2.1 using dynamic network sep and selective trace

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Data Management System Atsushi Hatada, Masakatsu Nishigaki, Masakazu Soga, Akio Takubo, and Itsukazu
(e-mail: cs7072@c.s.inf.shizuoka.ac.jp)M. Nishigaki is with the Faculty of Information, Shizuoka
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and Boundary-Fit Methods N. Chiba*I. Nishigaki*Y. Yamashita*C. Takizawa*K.
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